

EAST - [Untitled-1] 

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↳ S1: (295) assert$3 same (de-assert$3 or deassert$3) same (ready adj2 signal$2)
↳ S2: (144) "710"/$.cccls. and S1
↳ S3: (4) bi-directional same S1
↳ S4: (42) synchron$8 same S1
↳ S5: (21) communicat$3 same S1
↳ S6: (107) assert$3 same (de-assert$3 or deassert$3) same (ready adj3 (communicat$3 or transfer$4))
↳ S7: (86) signal$2 same S6
↳ S8: (64) "710"/$.cccls. and S7
↳ S9: (843) simultaneous adj2 (bidirectional or bi-directional)
↳ S10: (13) port$2 same S9 same synchron$8
↳ S11: (3) simultaneous same (bidirectional or bi-directional) same (pullup or pull-up) same (pulld...
↳ S12: (137) (bidirectional or bi-directional) same (pullup or pull-up) same (pulldown or pull-down)
↳ S13: (23) simultaneous and S12
↳ S14: (1) impedance and "6127849".pn.
↳ S15: (2) hysteresis and S11
↳ S16: (0) trigger$4 and "6127849".pn.
↳ S17: (0) schmitt and "6127849".pn.
↳ S18: (0) hysteresis and "6127849".pn.
↳ S19: (0) ((bidirectional adj port) and (initialization adj circuit) and synchron$8 adj circuit).clm.
↳ S20: (0) ((bidirectional adj port) and (initialization adj circuit) and (synchron$8 adj circuit))...
↳ S21: (1) complets$3 and "6127849".pn.
↳ S22: (8) ((bi-directional or bidirectional) adj2 port) same (hysteresis or schmitt)
↳ S23: (16) simultaneous and S23
↳ S24: (106) ((bi-directional or bidirectional) adj2 port) and (hysteresis or schmitt)
↳ S27: (295) assert$3 same (de-assert$3 or deassert$3) same (ready adj2 signal$2)
↳ S28: (4) bi-directional same S27
↳ S29: (1) (hysteresis or schmitt) and "5860134".pn.
↳ S30: (3) simultaneous same (bidirectional or bi-directional) same (pullup or pull-up) same (pulld...
↳ S31: (3) processor and S30
↳ S32: (20668) (close$2 adj loop) adj2 control
↳ S33: (93) (close$2 adj loop) adj2 impedance
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